



GENERAL DESCRIPTION

Item	Specification	Unit
Display Mode	TFT Transmissive	-
Number of Pixels	240(W) x RGB x 320(H)	-
Viewing Direction	12 o'clock	-
Driver/Control IC	ILI9325	-
Outline Dimension	40(W) x54.36(H) x2.7Max(T)	mm
Active Area	33.48(W) x 44.64(H)	mm
Pixel Pitch	0.1395(W) x 0.1395(H)	mm

DC ELECTRICAL CHARACTERISTICS(Ta=25°C)

Item	Symbol	Min.	Type.	Max.	Unit
Supply Voltage	VDD-VSS	2.8	-	3.3	V
Supply Voltage	VCI-V0	-	2.8	3.3	V
Supply Current	IDD	-	-	15	mA
Gate on Voltage	VGH	-	-	18	V
	VGI	-	-	-6.0	V
BL Forward Voltage	VF	-	-	3.3	V
BL Forward Current	IF	-	60	-	mA

PIN DESCRIPTION

No.	Symbol	I/O	Function																																																																				
1	IM0	I	<table border="1"> <thead> <tr> <th colspan="2">MPU-Interface Mode</th> <th>DB Pin in Use</th> </tr> </thead> <tbody> <tr> <td>0</td> <td>0</td> <td>Setting invalid</td> </tr> <tr> <td>0</td> <td>0</td> <td>0</td> <td>Setting invalid</td> </tr> <tr> <td>0</td> <td>0</td> <td>1</td> <td>Setting invalid</td> </tr> <tr> <td>0</td> <td>0</td> <td>1</td> <td>0</td> <td>ISO-system 16-bit interface</td> <td>DB[17:10], DB[6:1]</td> </tr> <tr> <td>0</td> <td>0</td> <td>1</td> <td>1</td> <td>ISO-system 8-bit interface</td> <td>DB[17:10]</td> </tr> <tr> <td>0</td> <td>1</td> <td>0</td> <td>0</td> <td>Serial Peripheral Interface (SPI)</td> <td>SDI, SDO</td> </tr> <tr> <td>0</td> <td>1</td> <td>1</td> <td>*</td> <td>Setting invalid</td> <td></td> </tr> <tr> <td>1</td> <td>0</td> <td>0</td> <td>0</td> <td>Setting invalid</td> <td></td> </tr> <tr> <td>1</td> <td>0</td> <td>0</td> <td>1</td> <td>Setting invalid</td> <td></td> </tr> <tr> <td>1</td> <td>0</td> <td>1</td> <td>0</td> <td>ISO-system 16-bit interface</td> <td>DB[17:0]</td> </tr> <tr> <td>1</td> <td>0</td> <td>1</td> <td>1</td> <td>ISO-system 8-bit interface</td> <td>DB[17:0]</td> </tr> <tr> <td>1</td> <td>1</td> <td>-</td> <td>-</td> <td>Setting invalid</td> <td></td> </tr> </tbody> </table>	MPU-Interface Mode		DB Pin in Use	0	0	Setting invalid	0	0	0	Setting invalid	0	0	1	Setting invalid	0	0	1	0	ISO-system 16-bit interface	DB[17:10], DB[6:1]	0	0	1	1	ISO-system 8-bit interface	DB[17:10]	0	1	0	0	Serial Peripheral Interface (SPI)	SDI, SDO	0	1	1	*	Setting invalid		1	0	0	0	Setting invalid		1	0	0	1	Setting invalid		1	0	1	0	ISO-system 16-bit interface	DB[17:0]	1	0	1	1	ISO-system 8-bit interface	DB[17:0]	1	1	-	-	Setting invalid	
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2	IM1	I																																																																					
3	IM2	I																																																																					
4	IM3	I																																																																					
5	/RES	I	Chip reset input pin.																																																																				
6	VSYNC	I	Frame synchronizing signal																																																																				
7	HSYNC	-	Line synchronizing signal.																																																																				
8	DOTCLK	-	Dot clock signal.																																																																				
9	ENABLE	-	Data ENABLE signal.																																																																				
10~27	D17~D0	I/O	Data bus																																																																				
28	/RD	I	Read Data Select Input pin.																																																																				
29	/WR,SCL	I	/WR: Write signal.																																																																				
			SCL: SPI clock signal																																																																				
30	DC	I	Data/Command select pin.																																																																				
31	SDO	O	SPI output pin.																																																																				
32	SDI	-	SPI input pin.																																																																				
33	FMARK	O	Output a frame head pulse signal.																																																																				
34	/CS	I	Chip Select Input pin.																																																																				
35	VDD	P	Power pin.																																																																				
36	VSS	P	Power Ground.																																																																				
37	A	I	B/L Power input pin anode.																																																																				
38	K1	I	B/L Power input pin negative.																																																																				
39	K2	I	B/L Power input pin negative.																																																																				
40	K3	I	B/L Power input pin negative.																																																																				

PRODUCT PICTURE



Mechanical Specification

